Boost LED Driver

Features

- Input voltage range: 2.2 to 6V
- Programmable LED Current
- Drives LEDs Up to 27V
- Switching Frequency: up to 800KHz
- > Programmable Overvoltage Protection
- ➤ Tiny DFN3*3-8L Package

Applications

- ➤ PDA
- Cellular Phones
- > Handheld Devices
- MP3 Players
- GPS Receivers

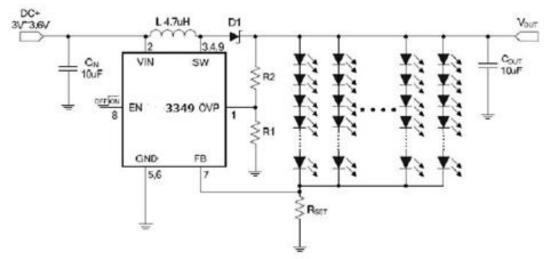
Description

The JTM3349 is a step-up DC/DC converter with a constant current to drive white LEDs or similar. The device can drive up to eight LEDs in series from a Li-lon cell. The LED current is set by the external resistor (Rset) and is directly regulated by the feedback (FB) voltage (typ: 200 mV) across the external resistor Rset.

To ensure maximum safety during operation, the output has integrated overvoltage protection that prevents damage to the device in case of some fault conditions. The OVP voltage can be programmed by two external resistors.

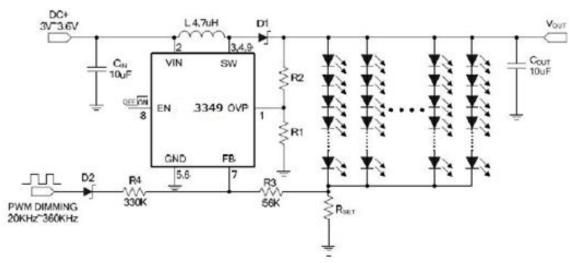
The JTM3349 is available in a tiny DFN3*3-8L package.

Typical Application Circuit

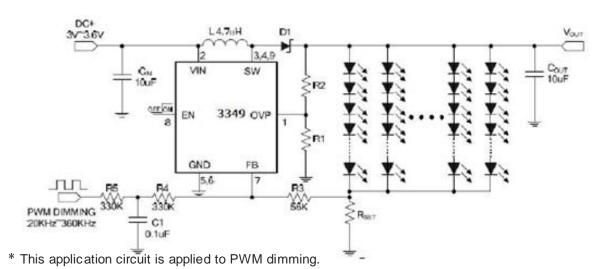


^{*} ILED=200mV/ RSET

^{*} VOUT < VOVP * (1+R2/R1)



* This application circuit is applied to PWM dimming.



Absolute Maximum Ratings (Note1)

Supply Voltage	7V
SW Voltage	30V
FB Voltage	7V
EN Voltage	7V
OVP Voltage	7V
Operating Temperature Range (Note 2)	40°C to 85°C
Maximum Junction Temperature	125°C
Storage Temperature Range	65°C to 150°C
Lead Temperature (Soldering, 10 sec)	265°C

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The JTM3349 is guaranteed to meet performance specifications from 0°C to 85°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Pin Assignment

Top View	PIN	NAME	DESCRIPTION
	1	OVP	Overvoltage protection Pin
2 7	2	VIN	Input Supply Pin
3 [6]	3,4,9	SW	Switch Output Pin
4 5	5,6	GND	Ground
DFN 3*3-8L	7	FB	Feedback Pin
2	8	EN	ON/OFF Control (High Enable)

^{*}EXPOSED PAD (PIN 9) IS SW, MUST BE SOLDERED TO PCB.

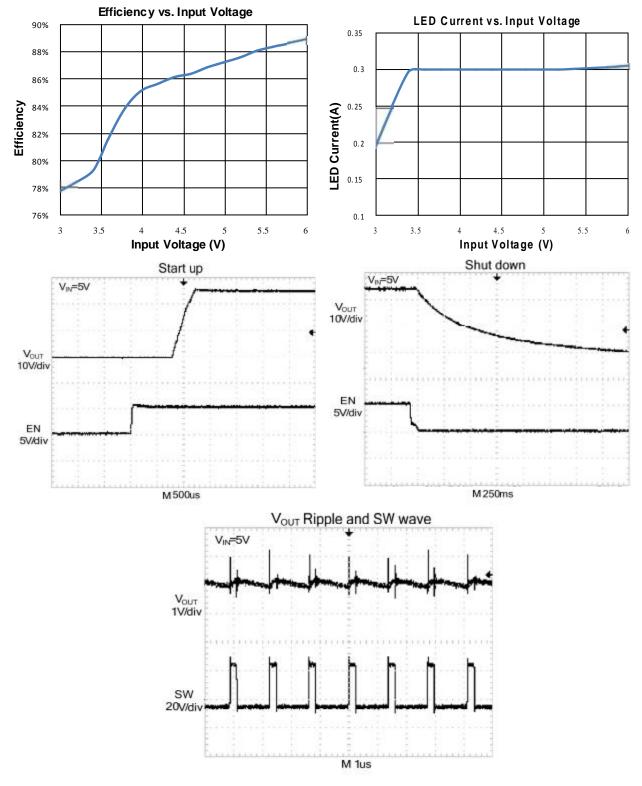
Electrical Characteristics

Operating Conditions: Vin = 3V, TA = 25°C, unless otherwise specified.

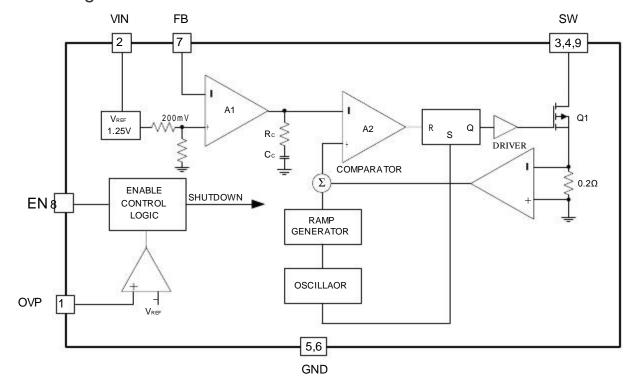
SYMBOL	PARAMETE R	CONDITIONS	MIN	TYP	MAX	UNITS
Vin	Operating Voltage Range		2.2		6	V
VFB	Feedback Voltage	TA = 25℃, ILOAD = 1mA		200		mV
lғв	FB Pin Bias Current	EN=GND		60		nA
lα	Operating quiescent current into VIN	Iουτ = 0 mA, not switching		100		uA
ISD(OUT)	Shutdown current			20		uA
Fsw	Switching Frequency			800		KHz
Vsw	Maximum switch voltage			30		V
Rds(ON)	MOSFET on-resistance			50		mΩ
DC	Maximum Duty Cycle			90		%
Vovp	Output overvoltage protection	Vout rising		1.212		V
VENL	EN Falling Threshold			0.5		٧
VENH	EN Rising Threshold			0.7		V

Typical Performance Characteristics

Operating Conditions: TA = 25 $^{\circ}$ C, VIN=5V, RSET=0.68 Ω , 9PCS LED Series, unless otherwise specified.



Block Diagram



Pin Functions

PIN	NAME	FUNCTION
1	OVP	Overvoltage protection Pin. Programs VOUT overvoltage protection level (OVP) to protect device.
2	VIN	Input Supply Pin. Must be locally bypassed.
3,4,9	SW	Switch Pin. Connect inductor/diode here. Minimize trace area at this pin to reduce EMI.
5,6	GND	Ground Pin.
7	FB	Feedback Pin. Reference voltage is 200mV. Connect cathode of lowest LED and resistor here. Calculate resistor value according to the formula: Rset = 200mV/ILED.
8	EN	Shutdown Pin. Connect to 1.5V or higher to enable device; 0.5V or less to disable device.

Application Information

Operation

The JTM3349 operates in a constant frequency, current mode like a standard boost converter but regulates the voltage across the external resistor (Rset) instead of the output voltage. This gives an accurate regulated LED current independent of the input voltage and number of LEDs connected. With integrated overvoltage protection (OVP), the JTM3349 is configured as a current source with overvoltage protection ideally suited to drive LEDs. The device can drive up to eight serial LEDs with the internal switch.

LED Current Control

The LED current is controlled by the feedback resistor (Rset). The feedback reference is 200mV. The LED current is 200mV/ Rset. In order to have accurate LED current, precision resistors are preferred (1% is recommended).

Dimming Control

With the PWM signal applied to the FB pin, the JTM3349 is turned on or off by the PWM signal. The LEDs operate at either zero or full current. The average LED current increases proportionally with the duty cycle of the PWM signal. A 0% duty cycle will turn off the JTM3349 and corresponds to zero LED current. A 100% duty cycle corresponds to full current. The typical frequency range of the PWM signal is 20kHz to 360kHz. These are two kinds of dimming control circuits shown in figure 1 and figure 2.

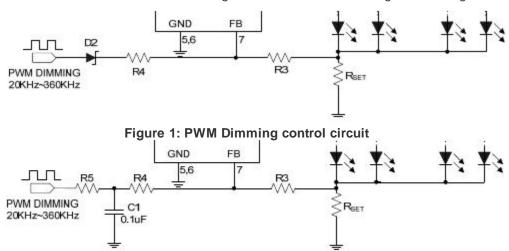


Figure 2: PWM Dimming control circuit

Diode Selection

The high switching frequency of JTM3349 demands a high-speed rectifier diode. For most applications, Schottky diodes are recommended because of their fast recovery time and low forward voltage. Moreover, the diode reverse breakdown voltage must exceed Vout. In general, use a high-speed silicon rectifier diode with adequate reverse voltage.

Inductor Selection

The selection of the inductor together with the nominal LED current, input, and output voltage of the

application determines the switching frequency of the converter. Because of the JTM3349's high switching frequency, inductors with a ferrite core or equivalent are recommended. Powdered iron cores are not recommended due to their high losses at frequencies over 50KHz.

Output and Input Capacitors Selection

Larger output capacitors reduce noise and improve load-transient response, stability, and power-supply rejection. Since output ripple in boost DC-DC designs is dominated by capacitor equivalent series resistance (ESR), Low-ESR capacitors must be used.

The input capacitor (CIN) reduces the current peaks caused by the input supply and reduces noise injection. Its value is largely determined by the source impedance of the input supply. High source impedance requires high input capacitance, particularly at the input voltage falls. At low input voltage, increasing CIN or lowering its ESR can improve efficiency. Using the same capacitance value for CIN as for COUT is a good start.

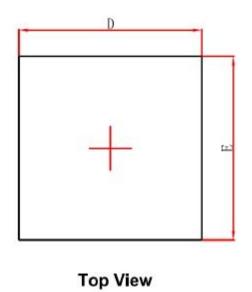
The Cout ESR affects loop stability by introducing a left half-plane zero. A small capacitor C1 between FB and GND forms a pole with the feedback resistance that cancels the ESR zero. This small capacitor C1 and provide sufficient compensation for the loop.

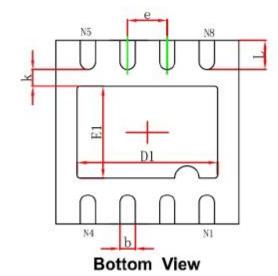
Overvoltage Protection (OVP)

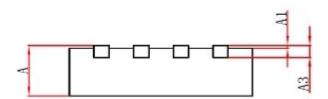
As with any current source, the output voltage rises as the output impedance increases or is disconnected. To prevent the output voltage from exceeding the maximum main switch (Q1) voltage rating of 30V, an overvoltage protection circuit is integrated. When the output voltage exceeds the OVP threshold voltage, (Q1) turns off. The converter switch remains off until the output voltage falls below the OVP threshold voltage. As long as the output voltage is below the OVP threshold the converter continues its normal operation, until the output voltage exceeds the OVP threshold again.

Packaging Information

DFN3*3-8L Package Outline Dimension







Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
А	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035	
A1	0.000	0.050	0.000	0.002	
А3	0.203REF		0.008	BREF	
D	2.900	3.100	0.114	0.122	
Е	2.900	3.100	0.114	0.122	
D1	2.200	2.400	0.087	0.094	
E1	1.400	1.600	0.055	0.063	
k	0.200MIN		0.00	8MIN	
b	0.180	0.300	0.007	0.012	
е	0.650TYP		0.026TYP		
L	0.375	0.575	0.015	0.023	