

Features

Wide 4.5V to 40V Input Voltage Range
3.3V, 5V, 12V, and adjustable versions
Output Adjustable from 1.23V to 37V
Maximum Duty Cycle 100%
Minimum Drop Out 1.5V
Fixed 150KHz Switching Frequency
2A Constant Output Current Capability
Internal Optimize Power Transistor
High efficiency
Excellent line and load regulation
TTL shutdown capability
ON/OFF pin with hysteresis function
Built in thermal shutdown function
Built in current limit function
Built in second current limit function
Available in SOIC8 package

Applications

LCD Monitor and LCD TV
Digital Photo Frame
Set-up Box
ADSL Modem
Telecom / Networking Equipment

General Description

The JTM1509 is a 150 KHz fixed frequency PWM buck (step-down) DC/DC converter, capable of driving a 2A load with high efficiency, low ripple and excellent line and load regulation. Requiring a minimum number of external components, the regulator is simple to use and include internal frequency compensation and a fixed-frequency oscillator.

The PWM control circuit is able to adjust the duty ratio linearly from 0 to 100%. An enable function, an over current protection function is built inside. When second current limit function happens, the operation frequency will be reduced from 150KHz to 50KHz. An internal compensation block is built in to minimize external component count.



SOIC-8

Figure1. Package Type of JTM1509

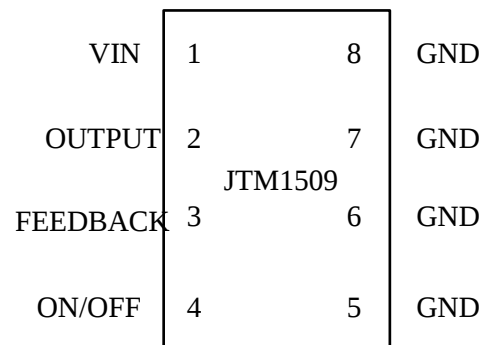
Pin Configurations

Figure2. Pin Configuration of JTM1509 (Top View)

Table 1 Pin Description

Pin Number	Pin Name	Description
1	VIN	Supply Voltage Input Pin. JTM1509 operates from a 4.5V to 40V DC voltage. Bypass Vin to GND with a suitably large capacitor to eliminate noise on the input.
2	OUTPUT	Power Switch Output Pin (SW). Output is the switch node that supplies power to the output.
5~8	GND	Ground Pin. Care must be taken in layout. This pin should be placed outside of the Schottky Diode to output capacitor ground path to prevent switching current spikes from inducing voltage noise into JTM1509.
3	FEEDBACK	Feedback Pin (FB). Through an external resistor divider network, Feedback senses the output voltage and regulates it. The feedback threshold voltage is 1.23V.
4	ON/OFF	Enable Pin. Drive ON/OFF pin low to turn on the device, drive it high to turn it off. Floating is default low.

Function Block

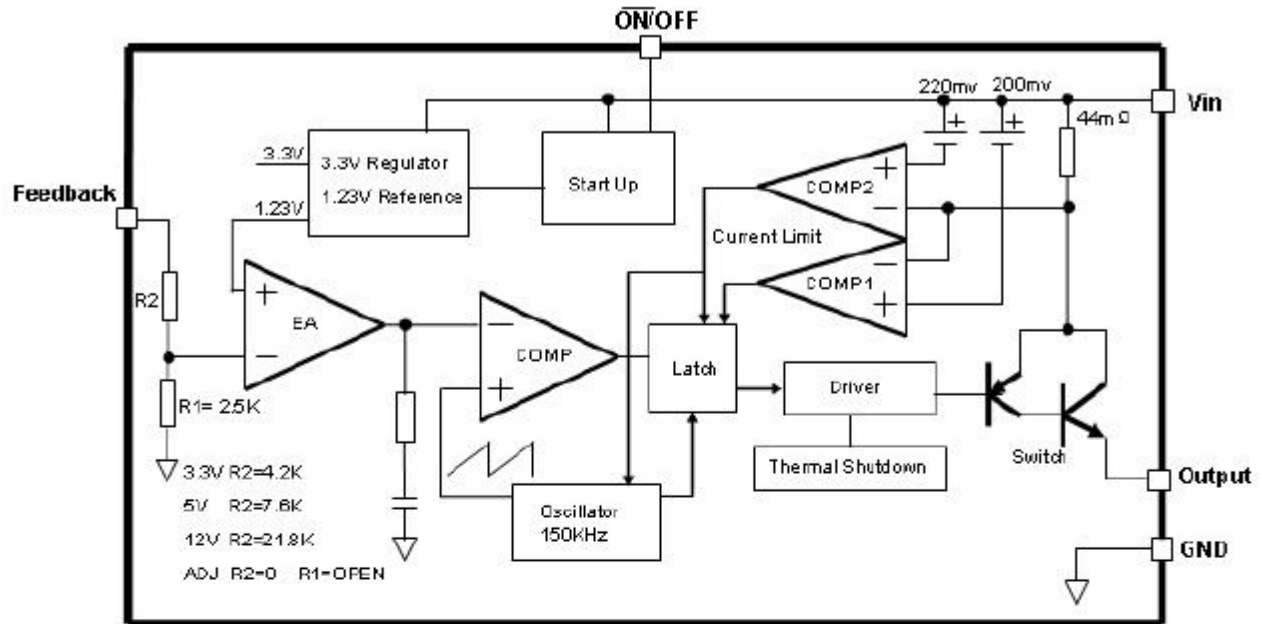


Figure3. Function Block Diagram of JTM1509

Typical Application Circuit

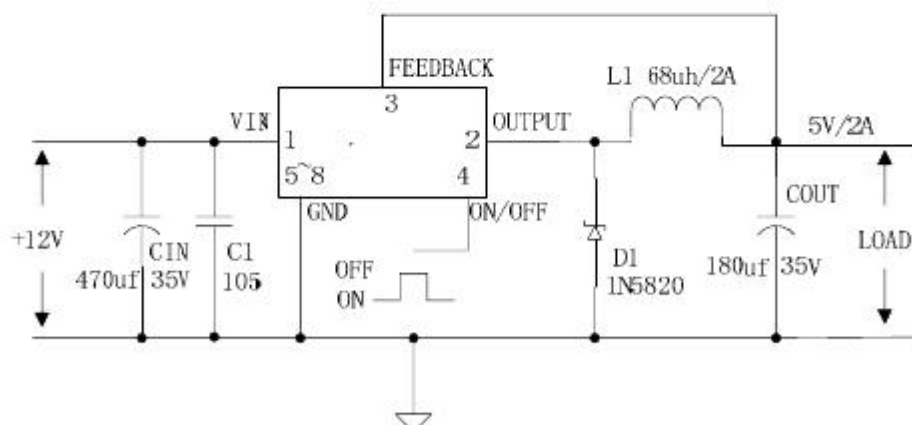
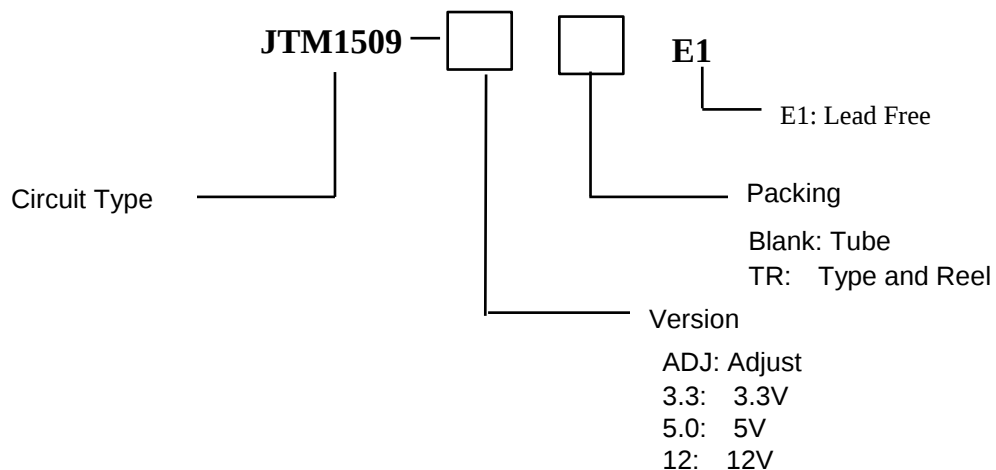


Figure4. JTM1509 Typical Application Circuit 12V-5V/2A

Ordering Information

Package	Temperature Range	Part Number	Marking ID	Packing Type
		Lead Free	Lead Free	
SOIC8	-40 C ~ 85 C	JTM1509-ADJ	JTM1509-ADJ	Tube
		JTM1509-3.3	JTM1509-3.3	Tube
		JTM1509-5.0	JTM1509-5.0	Tube
		JTM1509-12	JTM1509-12	Tube
		JTM1509-ADJTR	JTM1509-ADJ	Tape & Reel
		JTM1509-3.3TR	JTM1509-3.3	Tape & Reel
		JTM1509-5.0TR	JTM1509-5.0	Tape & Reel
		JTM1509-12TR	JTM1509-12	Tape & Reel

Absolute Maximum Ratings (Note1)

Parameter	Symbol	Value	Unit
Input Voltage	V _{in}	-0.3 to 45	V
Feedback Pin Voltage	V _{FB}	-0.3 to V _{in}	V
ON/OFF Pin Voltage	V _{ON/OFF}	-0.3 to V _{in}	V
Output Switch Pin Voltage	V _{Output}	-0.3 to V _{in}	V
Power Dissipation	P _D	Internally limited	mW
Thermal Resistance (SOIC8) (Junction to Ambient, No Heatsink, Free Air)	R _{JA}	100	℃/W
Operating Junction Temperature	T _J	-40 to 125	℃
Storage Temperature	T _{STG}	-65 to 150	℃
Lead Temperature (Soldering, 10 sec)	T _{LEAD}	260	℃
ESD (HBM)		2000	V

Note1: Stresses greater than those listed under Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

JTM1509-3.3 Electrical CharacteristicsT_a = 25℃;unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<i>System parameters test circuit figure5</i>						
V _{OUT}	Output Voltage	V _{in} = 4.75V to 40V I _{load} =0.2A to 2A	3.168	3.3	3.432	V
Efficiency	η	V _{in} =12V ,V _{out} =3.3V I _{out} =2A	-	75	-	%

JTM1509-5.0 Electrical CharacteristicsT_a = 25℃;unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<i>System parameters test circuit figure5</i>						
V _{OUT}	Output Voltage	V _{in} = 7V to 40V I _{load} =0.2A to 2A	4.8	5	5.2	V
Efficiency	η	V _{in} =12V ,V _{out} =5V I _{out} =2A	-	82	-	%

JTM1509-12 Electrical CharacteristicsT_a = 25℃;unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<i>System parameters test circuit figure5</i>						
V _{OUT}	Output Voltage	V _{in} = 15V to 40V I _{load} =0.2A to 2A	11.52	12	12.48	V
Efficiency	η	V _{in} =25V ,V _{out} =12V I _{out} =2A	-	90	-	%

JTM1509-ADJ Electrical CharacteristicsT_a = 25℃;unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<i>System parameters test circuit figure5</i>						
V _{OUT}	Output Voltage	V _{in} = 4.5V to 40V I _{load} =0.2A to 2A	1.193	1.23	1.267	V
Efficiency	η	V _{in} =12V ,V _{out} =3V I _{out} =2A	-	74	-	%

Electrical Characteristics (DC Parameters)

V_{in} = 12V for the 3.3V,5V,and Adjustable versions and V_{in}=24V for the 12V version, GND=0V, V_{in} & GND parallel connect a 220uf/50V capacitor; I_{out}=500mA, T_a = 25°C; the others floating unless otherwise specified.

Parameters	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input operation voltage	V _{in}		4.5		40	V
Shutdown Supply Current	I _{STBY}	V _{ON/OFF} =5V		80	200	uA
Quiescent Supply Current	I _q	V _{ON/OFF} =0V, V _{FB} =V _{in}		2	10	mA
Oscillator Frequency	F _{osc}		127	150	173	KhZ
Switch Current Limit	I _L	V _{FB} =0		4		A
ON/OFF Pin Threshold	V _{ON/OFF}	High (Regulator OFF) Low (Regulator ON)		1.4 0.8		V
ON/OFF Pin Input Leakage Current	I _H	V _{ON/OFF} =2.5V (OFF)		5	15	uA
	I _L	V _{ON/OFF} =0.5V (ON)		0.2	5	uA
Output Saturation Voltage	V _{CE}	V _{FB} =0V I _{out} =2A		1.2	1.4	V
Max. Duty Cycle	D _{MAX}	V _{FB} =0V		100		%

Test Circuit and Layout guidelines

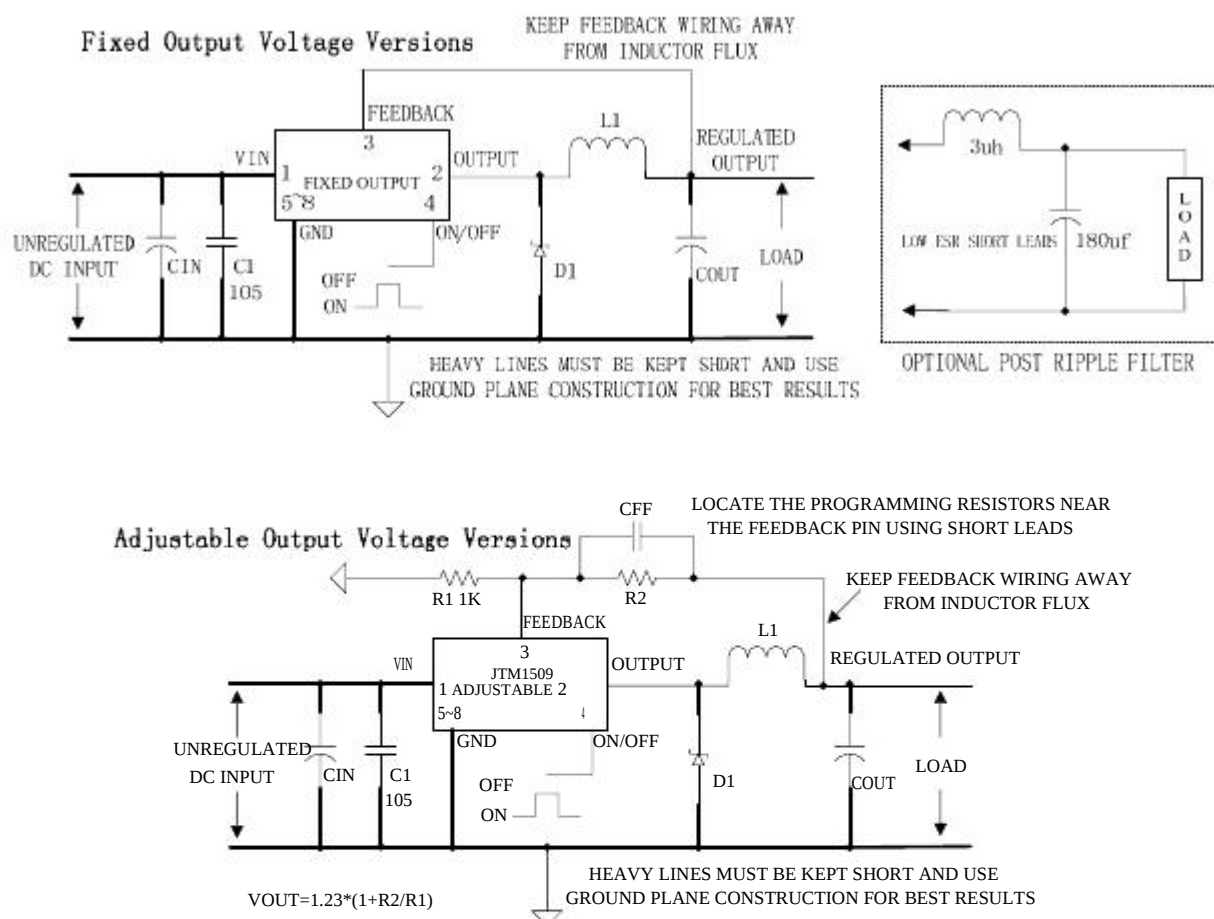


Figure5. Standard Test Circuits and Layout Guides

Select R1 to be approximately 1K, use a 1% resistor for best stability.

C1 and CFF are optional; in order to increase stability and reduce the input power line noise, CIN and C1 must be placed near to PIN1 and PIN3;

For output voltages greater than approximately 10V, an additional capacitor CFF is required. The compensation capacitor is typically between 100 pf and 33 nf, and is wired in parallel with the output voltage setting resistor, R2. It provides additional stability for high output voltage, low input-output voltages, and/or very low ESR output capacitors, such as solid tantalum capacitors. $CFF = 1/(31 \cdot 1000 \cdot R2)$; This capacitor type can be ceramic, plastic, silver mica, etc. (Because of the unstable characteristics of ceramic capacitors made with Z5U material, they are not recommended.)

JTM1509 Series Buck Regulator Design Procedure (Fixed Output)

Conditions			Inductor (L1)	Output Capacitor (COUT)			
				Through Hole Electrolytic		Surface Mount Tantalum	
Output Voltage (V)	Load Current (A)	Max Input Voltage (V)	Inductance (uh)	Panasonic HFQ Series (uf/V)	Nichicon PL Series (uf/V)	AVX TPS Series (uf/V)	Sprague 595D Series (uf/V)
3.3	2	6	22	470/25	470/35	330/6.3	390/6.3
		10	33	330/35	330/35	330/6.3	390/6.3
		40	47	330/35	270/50	220/10	330/10
5	2	9	22	470/25	560/16	220/10	330/10
		20	68	180/35	180/35	100/10	270/10
		40	68	180/35	180/35	100/10	270/10
12	2	15	33	330/25	330/25	100/16	180/16
		20	68	180/25	180/25	100/16	120/20
		40	150	82/25	82/25	68/20	68/25

JTM1509 Series Buck Regulator Design Procedure (Adjustable Output)

Output Voltage (V)	Through Hole Output Electrolytic			Surface Mount Output Capacitor		
	Panasonic HFQ Series (uf/V)	Nichicon PL Series (uf/V)	Feedforward Capacitor	AVX TPS Series (uf/V)	Sprague 595D Series (uf/V)	Feedforward Capacitor
2	820/35	820/35	33nf	330/6.3	470/4	33nf
4	560/35	470/35	10nf	330/6.3	390/6.3	10nf
6	470/25	470/35	3.3nf	220/10	330/10	3.3nf
9	330/25	330/25	1.5nf	100/16	180/16	1.5nf
12	330/25	330/25	1nf	100/16	180/16	1nf
15	220/25	220/35	680pf	68/20	120/20	680pf
24	220/35	150/35	560pf	33/25	33/25	220pf
28	100/50	100/50	390pf	10/35	15/50	220pf

Schottky Diode Selection Table

Current	Surface Mount	Through Hole	VR (The same as system maximum input voltage)				
			20V	30V	40V	50V	60V
1A		√	1N5817	1N5818	1N5819		
3A		√	1N5820	1N5821	1N5822		
		√	MBR320	MBR330	MBR340	MBR350	MBR360
	√		SK32	SK33	SK34	SK35	SK36
	√			30WQ03	30WQ04	30WQ05	
		√		31DQ03	31DQ04	31DQ05	
		√	SR302	SR303	SR304	SR305	SR306

Typical System Application for 3.3V Version

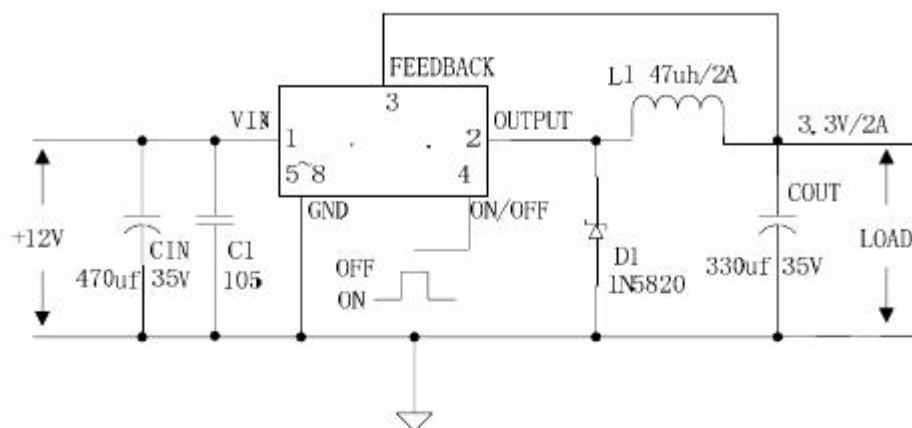


Figure6. JTM1509-3.3 System Parameters Test Circuit

Typical System Application for 5V Version

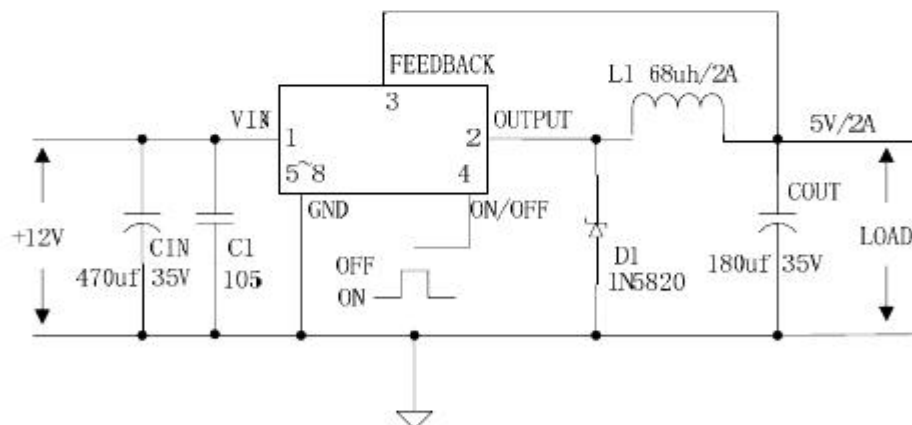


Figure7. JTM1509-5.0 System Parameters Test Circuit

Typical System Application for 12V Version

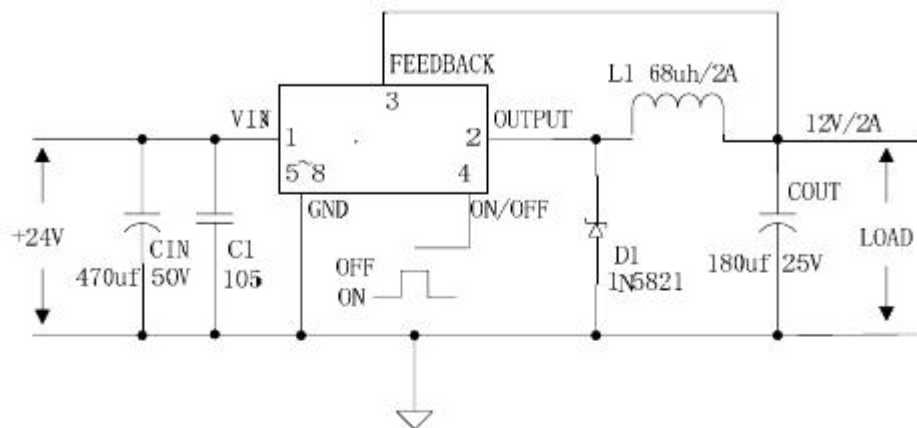


Figure8. JTM1509-12 System Parameters Test Circuit

Typical System Application for ADJ Version

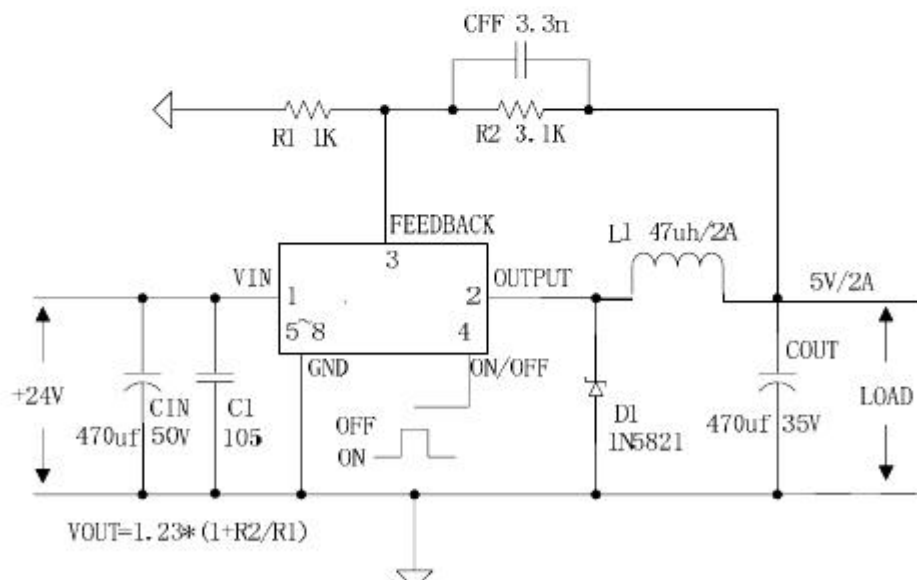


Figure9. JTM1509-ADJ System Parameters Test Circuit

Package Information

SOP8 Package Mechanical Dimensions

