N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

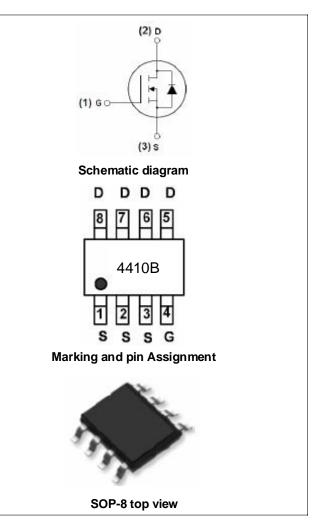
The JTM4410B uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. It can be used in a wide variety of applications.

GENERAL FEATURES

- VDS =30V,ID =12A
 RDS(ON) < 11mΩ @ VGS=10V
 RDS(ON) < 13mΩ @ VGS=4.5V
- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current

Application

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4410B	JTM4410B	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (TA=25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	lo	12	A
Drain Current-Continuous(Tc=100℃)	l⊳ (100℃)	7	А
Pulsed Drain Current	Ідм	60	А
Maximum Power Dissipation	PD	3	W
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case(Note 2)	Rejc	50	°C W	
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Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V Id=250µA	30	33	-	V	
Zero Gate Voltage Drain Current	IDSS	Vds=30V,Vgs=0V	-	-	1	μA	
Gate-Body Leakage Current	lgss	Vgs=±20V,Vds=0V	-	-	±100	nA	
On Characteristics (Note 3)	· · ·		•				
Gate Threshold Voltage	VGS(th)	Vos=Vgs,Io=250µA	1	1.8	3	V	
Drain-Source On-State Resistance		Vgs=10V, Id=12A	-	9	11	mΩ	
		Vgs=4.5V, Id=5A	-	11	13	11152	
Forward Transconductance	g FS	Vos=5V,Io=12A	15	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	Clss	Vos=15V,Vos=0V, F=1.0MHz	-	1550	-	PF	
Output Capacitance	Coss		-	300	-	PF	
Reverse Transfer Capacitance	Crss	T = 1.01011Z	-	180	-	PF	
Switching Characteristics (Note 4)	· · ·					•	
Turn-on Delay Time	td(on)		-	30	-	nS	
Turn-on Rise Time	tr	VDD=25V,ID=1A	-	20	-	nS	
Turn-Off Delay Time	td(off)	$V_{GS}=10V, R_{GEN}=6\Omega$	-	100	-	nS	
Turn-Off Fall Time	tr		-	80	-	nS	
Total Gate Charge	Qg	Vos=15V,Io=12A, Vos=5V	-	13	-	nC	
Gate-Source Charge	Qgs		-	5.5	-	nC	
Gate-Drain Charge	Qgd	v GS=0 V	-	3.5	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	Vsd	Vgs=0V,Is=12A	-	-	1.2	V	
Diode Forward Current (Note 2)	ls		-	-	12	A	

Electrical Characteristics (TA=25 $^\circ\!\!\mathrm{C}$ unless otherwise noted)

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

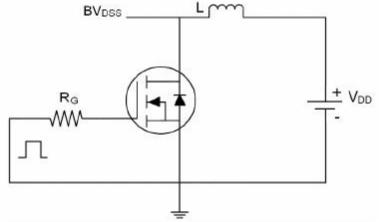
3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

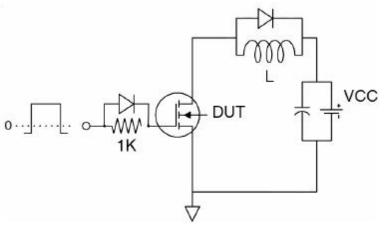
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Test circuit

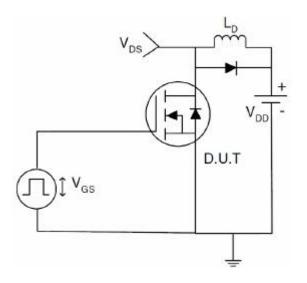
1) Eas test Circuits



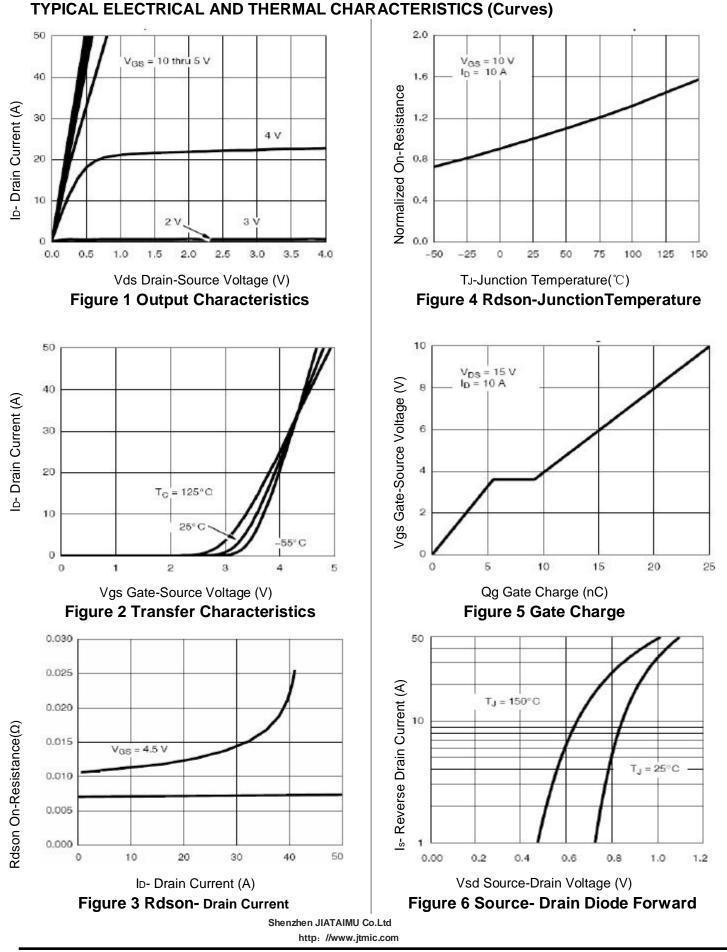
2) Gate charge test Circuit:



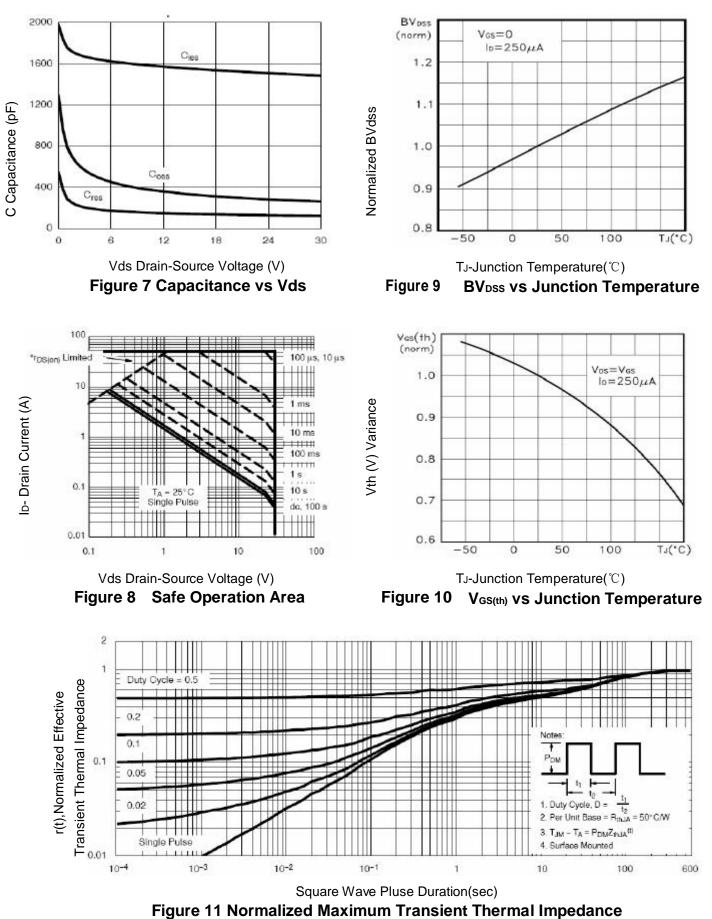
3) Switch Time Test Circuit:



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JTM4410B

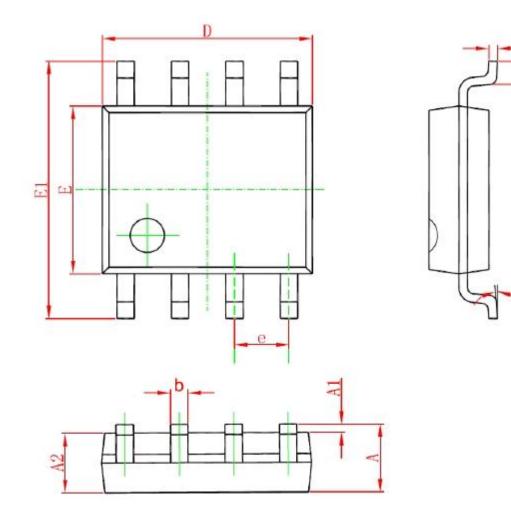


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SOP-8 PACKAGE IN FORMATION



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0. 250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0. 330	0. 510	0.013	0. 020	
С	0.170	0. 250	0.006	0.010	
D	4. 700	5. 100	0. 185	0.200	
E	3.800	4. 000	0.150	0. 157	
E1	5.800	6. 200	0. 228	0. 244	
e	1. 270 (BSC)		0.050	(BSC)	
L	0.400	1. 270	0.016	0.050	
θ	0°	8°	0°	8°	

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