

# JTMT9133 Series

## Step-Up DC/DC Converter-LED Backlight Driver

### ■GENERAL DESCRIPTION

The JTMT9133 series is a fixed frequency, constant current step-up DC/DC converter which is optimized for LED backlight applications in mobile phones, PDAs and digital cameras. Output voltage of up to 17.5V is possible so that four white LEDs can be driven in series. Since the LED current is set by only one external resistor, all white LEDs placed in series can be turned on at the same time. The new DC/DC Converter is also able to drive a network of two parallel banks of three LEDs.

LED dimming is controlled by adjusting the duty cycle of a PWM signal (10kHz Max.) applied to the CE pin.

Efficiency is high with a 0.2V low feedback reference voltage ensuring the  $R_{LED}$  losses are minimal. In addition, an internal MOSFET with a low  $R_{DS(on)}$  of 2.4Ω is used. A low profile and small board area solution can be achieved using a chip inductor and a small ceramic output capacitor  $C_L=0.22\mu F$  as a result of the high 1MHz switching frequency.

If white LEDs are opened or damaged, the detector built in the Lx pin causes the IC to stop oscillating, preventing eJTMTessive increase of the output voltage.

### ■APPLICATIONS

- For White LED drivers
- Smart phones / Mobile phones
- Mobile devices / terminals
- Digital still cameras

### ■FEATURES

**Input Voltage Range** : 2.5V ~ 6.0V

**Output Voltage Range** : Up to 17.5V externally set-up  
Reference voltage 0.2V +5%

**Oscillation Frequency** : 1.0MHz±20% —

**ON Resistance** : 2.4Ω

**High Efficiency** : 85%

3 white LEDs in series

$V_{IN}=3.6V$ ,  $I_{LED}=20mA$

**Control** : PWM control

**Stand-by Current** :  $I_{STB}=1.0\mu A$  (MAX.)

**Output Capacitor** : 0.22μF, ceramic

**Lx Limit Current** : 360mA (TYP.)

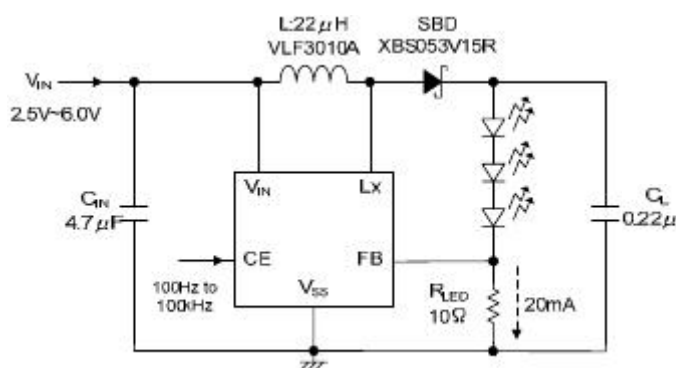
**Lx Overvoltage Limit** : 19V (TYP.)

**Operating Ambient Temperature** : -40°C ~ +85°C

**Packages** : SOT-25

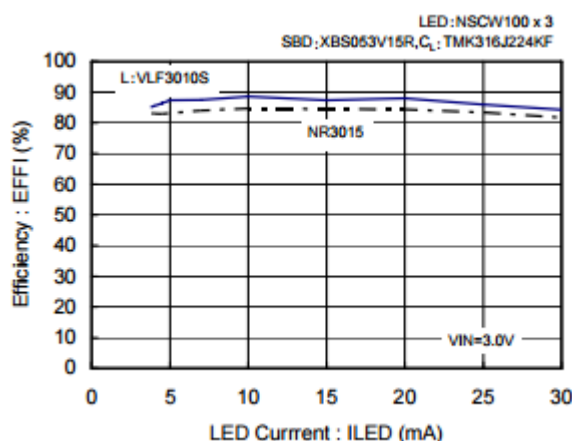
Environmentally Friendly : EU RoHS Compliant, Pb Free

### ■TYPICAL APPLICATION CIRCUIT

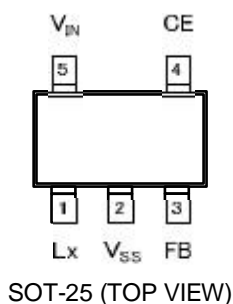


### ■TYPICAL PERFORMANCE CHARACTERISTICS

•JTMT9133B02A Series



## PIN CONFIGURATION



## PIN ASSIGNMENT

PIN NUMBER SOT-25	PIN NAME	FUNCTION
1	Lx	Switch
2	V <sub>SS</sub>	Ground
3	FB	Voltage Feedback
4	CE	Chip Enable
5	V <sub>IN</sub>	Power Input

## CE PIN FUNCTION

CE PIN	OPERATIONAL STATE
H	Operation
L	Shut-down

## PRODUCT CLASSIFICATION

### Ordering Information

JTMT9133①②③④⑤⑥-⑦

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①	Lx Overvoltage Limit	B	Available
②③	FB Voltage	02	0.2V
④	Oscillation Frequency	A	1MHz
⑤⑥-⑦ <sup>(*)</sup>	Package (Order Unit)	MR	SOT-25 (3,000/Reel)
		MR-G	SOT-25 (3,000/Reel)

<sup>†</sup> The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.



## ELECTRICAL CHARACTERISTICS

●JTMT9133B02AMR

Ta = 25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT.	CIRCUIT
FB Voltage	V <sub>FB</sub>		0.19	0.20	0.21	V	①
Output Voltage Range	V <sub>OUTSET</sub>		V <sub>IN</sub>	-	17.5	V	①
Input Voltage Range	V <sub>IN</sub>		2.5	-	6.0	V	①
Supply Current 1	I <sub>DD1</sub>		-	420	720	μA	②
Supply Current 2	I <sub>DD2</sub>	V <sub>IN</sub> =V <sub>Lx</sub> , FB=0.4V	-	60	140	μA	③
Stand-by Current	I <sub>STB</sub>	CE=0V, V <sub>Lx</sub> =5.0V	-	0	1.0	μA	③
Oscillation Frequency <sup>(*)</sup>	f <sub>OSC</sub>		0.8	1.0	1.2	MHz	②
Maximum Duty Cycle	MAXDTY		86	92	98	%	②
Efficiency <sup>(*)</sup>	EFFI	When connected to ext. components, V <sub>IN</sub> =3.6V, R <sub>LED</sub> =20Ω	-	85	-	%	①
Current Limit	I <sub>LIM</sub>	When connected to ext. components, V <sub>IN</sub> =3.6V	260	360	460	mA	④
Lx Overvoltage Limit	V <sub>LxOVL</sub>	Voltage which Lx pin voltage holding "High" level V <sub>IN</sub> ≥ 2.5V	18.0	19.0	22.0	V	②
Lx ON Resistance	R <sub>SWON</sub>	V <sub>IN</sub> =3.6V, V <sub>Lx</sub> =0.4V <sup>(*)</sup>	-	2.4		Ω	④
Lx Leakage Current	I <sub>LxL</sub>	Same as I <sub>STB</sub>	-	0.0	1.0	μA	③
CE High Voltage	V <sub>CEH</sub>	CE applied voltage when Lx starts oscillation	0.65	-	6.0	V	②
CE Low Voltage	V <sub>CEL</sub>	CE applied voltage which Lx pin voltage holding "High" level	V <sub>SS</sub>	-	0.2	V	②
CE High Current	I <sub>CEH</sub>	Same as I <sub>DD2</sub>	-0.1	-	0.1	μA	③
CE Low Current	I <sub>CEL</sub>	Same as I <sub>STB</sub>	-0.1	-	0.1	μA	③
FB High Current	I <sub>FBH</sub>	Same as I <sub>DD2</sub>	-0.1	-	0.1	μA	③
FB Low Current	I <sub>FBL</sub>	Same as I <sub>STB</sub>	-0.1	-	0.1	μA	③

NOTE: \*Test circuit ①: Unless otherwise stated, V<sub>IN</sub>=3.0V, V<sub>CE</sub>=3.0V, R<sub>LED</sub>=10Ω  
 \*Test circuit ②: Unless otherwise stated, V<sub>IN</sub>=3.0V, V<sub>CE</sub>=3.0V, V<sub>FB</sub>=0.0V, V<sub>PULL</sub>=5.0V, R<sub>PULL</sub>=100Ω  
 \*Test circuit ③: Unless otherwise stated, V<sub>IN</sub>=3.0V, V<sub>CE</sub>=3.0V, V<sub>FB</sub>=0.0V  
 \*Test circuit ④: Unless otherwise stated, V<sub>CE</sub>=3.0V, V<sub>PULL</sub>=5.0V

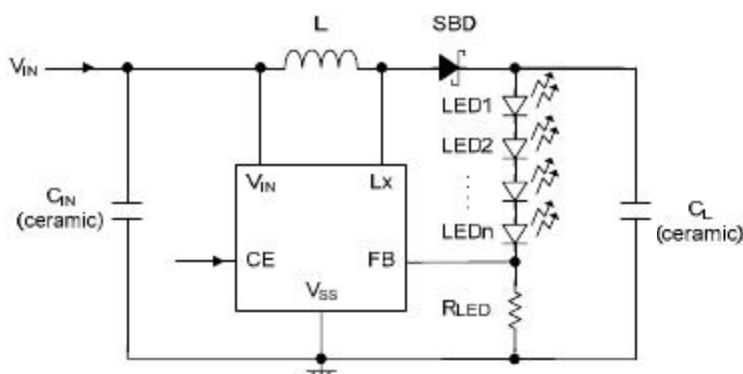
(\*) : The duty cycle is forcibly reduced when maximum duty cycle periods are repeated.

(\*) : LED NSPW310BS x 3, EFFI = {[(output voltage) x (output current)] / [(input voltage) x (input current)]} x 100

(\*) : V<sub>PULL</sub> is adjusted to make V<sub>Lx</sub> 0.4V when the driver transistor is turned on.

## ■ TYPICAL APPLICATION CIRCUITS

### ● JTMT9133B02A



## ■ EXTERNAL COMPONENTS

SYMBOL	VALUE	PART NUMBER	MANUFACTURER
L	22 $\mu$ H	VLF3010A-220MR	TDK
SBD <sup>(*)1</sup>	-	XBS053V15R <sup>(*)2</sup>	TOREX
		MA2Z720	PANASONIC
CIN	4.7 $\mu$ F	JMK107BJ475MA-B	TAIYO YUDEN
CL <sup>(*)3</sup>	0.22 $\mu$ F	TMK107BJ224KA-B	TAIYO YUDEN

NOTE:

\*1: Please use a Schottky barrier diode (SBD) with a low junction capacitance.

\*2: For using the XBS053V15R with four white LEDs in series, please be noted with a direct reverse voltage ( $V_R=20V$ ) and a repetitive peak reverse voltage ( $V_{RM}=30V$ ).

\*3: Use ceramic capacitors processing a low temperature coefficient.

## ■ OPERATIONAL EXPLANATION

The series consists of a reference voltage source, ramp wave circuit, error amplifier, PWM comparator, phase compensation circuit, Lx overvoltage limit circuit, N-channel MOS driver transistor, current limiter circuit and others. Phase compensation is performed on the resulting error amplifier output, to input a signal to the PWM comparator to determine the turn-on time during switching. The PWM comparator compares, in terms of voltage level, the signal from the error amplifier with the ramp wave from the ramp wave circuit, and delivers the resulting output to the N-channel MOS driver transistor to cause the Lx pin to output a switching duty cycle. This process is continuously performed to ensure stable output voltage. The current feedback circuit detects the N-channel MOS driver transistor's current for each switching operation, and modulates the error amplifier output signal. This enables a stable feedback loop even when a low ESR capacitor, such as a ceramic capacitor, is used, ensuring stable output voltage.

### <Reference Voltage Source>

The reference voltage source provides the reference voltage to ensure stable output voltage of the IC.

### <Ramp Wave Circuit>

The ramp wave circuit determines switching frequency. The 1MHz (TYP.) of frequency is fixed internally.

Clock pulses generated in this circuit are used to produce ramp waveforms needed for PWM operation.

### <Error Amplifier>

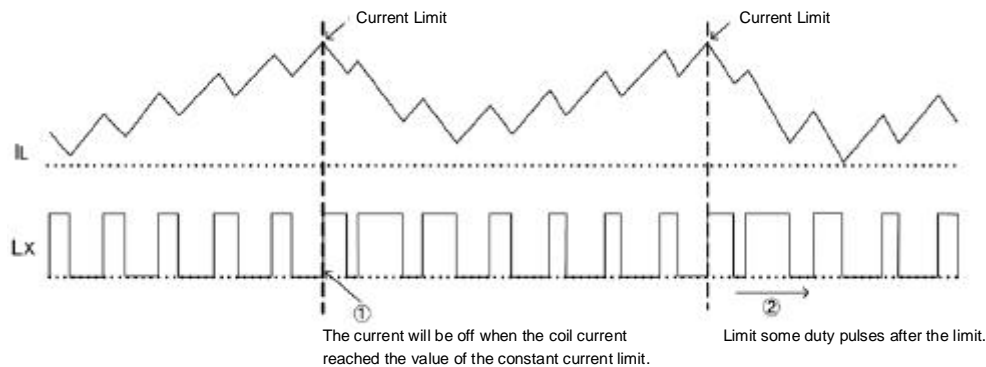
The error amplifier is designed to monitor output voltage. The amplifier compares the reference voltage with the FB pin voltage. When a feed-back voltage becomes lower than the reference voltage, an output voltage of the error amplifier is increased. Gain and frequency characteristics of the error amplifier output are fixed internally as an optimize signal.

## ■ OPERATIONAL EXPLANATIONS (Continued)

### <Current Limit>

The current limit circuit of the JTMT9133 series monitors the current flowing through the N-channel MOS driver transistor connected to the Lx pin, and features a combination of the constant-current type current limit mode and the duty cycle limit of the next pulse.

- ☞ When the driver current is greater than a specific levels, the constant-current type current limit function operates to turn off the pulses from the Lx pin at any given timing.
- 📄 The IC controls the next pulse to be smaller than the first pulse.



### <Lx Overvoltage Limit Circuit>

JTMT9133 series' Lx overvoltage limit circuit monitors the Lx pin voltage. When the Lx pin voltage exceeds 19V (TYP.), the IC performs the function of latching the OFF state of the driver transistor, and goes into operation suspension mode. In suspension mode, operations can be resumed by restoring power to the VIN pin. The suspension mode does not mean a complete shutdown, but a state in which pulse output is suspended; therefore, the internal circuitry remains in operation.

### <Maximum Duty Cycle Limit>

The JTMT9133 series' maximum duty cycle limit circuit monitors the duty cycle. When the maximum duty cycle is repeated for a certain time, the IC controls the error amplifier output so that the duty cycle of the next pulse becomes smaller than that of the first pulse.

### <CE Pin Function>

The operation of the JTMT9133 series will enter into the shut down mode when a low level signal is input to the CE pin. During the shut down mode, the supply current is 0 $\mu$ A (TYP.), with high impedance at the Lx pin. The IC starts its operation with a high level signal to the CE pin. The input to the CE/MODE pin is a CMOS input and the sink current is 0  $\mu$ A (TYP.). 100 $\mu$ s after disable, the IC goes into suspension mode and supply current is minimal. After this, the IC will be in stand-by mode and the supply current will be 0 $\mu$ A (TYP.).

## ■NOTES ON USE

<Lx (Pin 1): Switch Pin>

Please connect the anode of a Schottky barrier diode and an inductor to the Lx pin.

<FB (Pin 3): Voltage Feedback Pin>

The reference voltage is 200mV (TYP.). A resistor (R<sub>LED</sub>) should be connected to the FB pin for setting the cathode of LEDs and a constant current value. The resistance value can be calculated by the following equation.

$$R_{LED} = 0.2 / I_{LED}$$

I<sub>LED</sub>=Setting constant current value

Typical example:

I <sub>LED</sub>	R <sub>LED</sub>	I <sub>LED</sub>	R <sub>LED</sub>
5mA	40Ω	13.3mA	15Ω
10mA	20Ω	20mA	10Ω

<CE (Pin 4): Chip Enable Pin>

An ENABLED state is reached when the CE voltage eJTMTeeds 0.65V and a DISABLED state when the CE Voltage falls below 0.2V.

<V<sub>IN</sub> (Pin 5): Power Supply Pin>

Please connect an inductor and an input by-pass capacitor (C<sub>IN</sub>) to the V<sub>IN</sub> pin.

1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be eJTMTeeded.
2. JIATAIMU places an importance on improving our products and their reliability.

We request that users incorporate fail-safe designs and post-aging protection treatment when using JIATAIMU products in their systems.

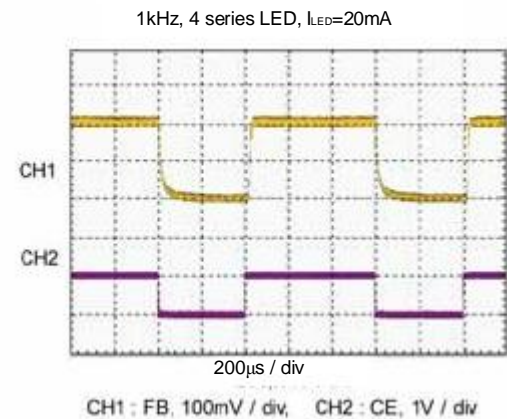
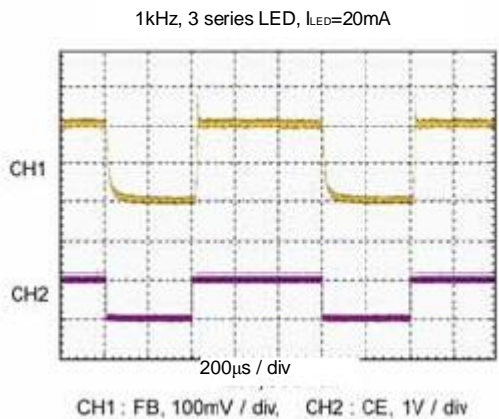
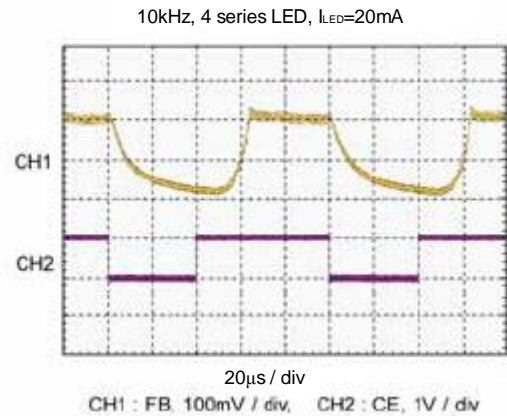
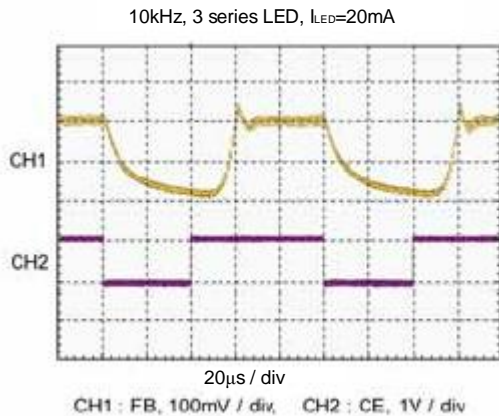
## APPLICATION INFORMATION

### <Dimming Control>

#### 1. Applying PWM signal to the CE pin

The JTMT9133 repeats on/off operations by a PWM signal applied to the CE pin. The magnitude of LED current,  $I_{LED}$ , when the diode is on, is determined by  $R_{LED}$ . The magnitude is zero when the diode is off. The average of LED current is proportional to the positive duty ratio of the PWM signal.

The frequency of the PWM signal can be controlled to the optimum value between 100Hz and 10kHz. With regard to the amplitude of the PWM signal, the high level should be higher than the "H" voltage of CE,  $V_{CEH}$ , and the low level, lower than the "L" voltage of CE,  $V_{CEL}$ .



#### 2. Step-Wise Regulation of LED Current

In some applications, it may be necessary to incorporate step-wise regulation of LED current,  $I_{LED}$ . Step-wise regulation of LED illumination is achieved by connecting a switch element SW1 in parallel with  $R_{LED}$  and in series with  $R_{LED1}$  and turning SW1 on and off, as shown below. Choose a resistance of  $R_{LED}$  so that the minimum necessary current is gained when switch element SW1 is off. The resistance of  $R_{LED1}$  should be such that a desired increase of current passed through the LED is gained when the switch element is on.

Ex.) Current  $I_{LED} = 5mA$  and  $15mA$   
 $R_{LED} = 200mV / 5mA = 40 \Omega$   
 $R_{LED1} = 200mV / (15mA - 5mA) = 20 \Omega$

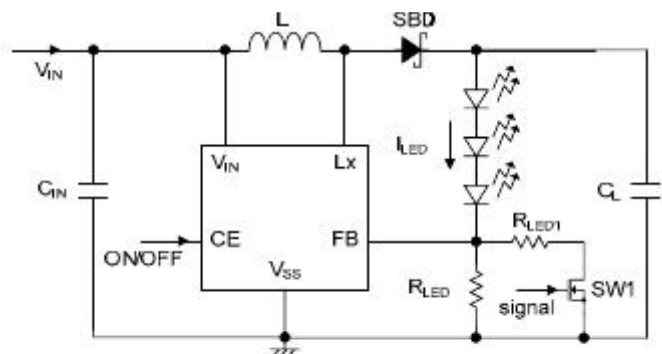


Figure Circuit using Step-wise Regulation of LED Current



## ■APPLICATION INFORMATION (Continued)

### <Dimming Control (Continued)>

#### 3. Using DC Voltage

If in an application it is necessary to control the LED current by a variable DC voltage, illumination control of LED is achieved by connecting R1 and R2 and applying a direct-current voltage to R2, as shown below.

When  $R1 \gg R_{LED}$ ,  $I_{LED}$  which flows into LEDs can be calculated by the following equation;

$$I_{LED} = (V_{REF} - R1 / R2 (V_{DC} - V_{REF})) / R_{LED}$$

$V_{REF} = 0.2V$  (TYP.)

Ex.1) When  $R1 = 10k\Omega$ ,  $R2 = 100k\Omega$ ,  $R_{LED} = 10\Omega$ ,  
In the range of 0.2V to 2.2V DC,  $I_{LED}$   
(LED current) varies between 20mA to 0mA.

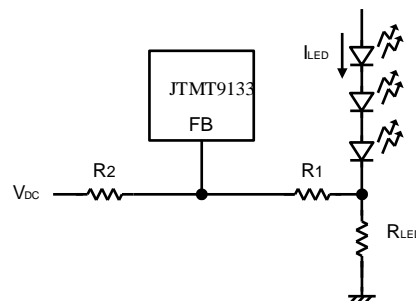


Figure Circuit using DC voltage

Ex.2) When  $R1 = 10k\Omega$ ,  $R2 = 100k\Omega$ ,  $R3 = 10k\Omega$ ,  
 $C1 = 0.1\mu F$ ,  $R_{LED} = 10\Omega$ , the average LED current will  
be 10mA by inputting a PWM signal of CE 'H' level:  
2.2V, CE 'L' level: 0V, duty cycle: 50%, oscillation  
frequency: 100Hz. As well as the way of dimming  
control by applying the PWM signal to the CE pin, the  
average LED current increases proportionally with the  
positive duty cycle of the PWM signal.

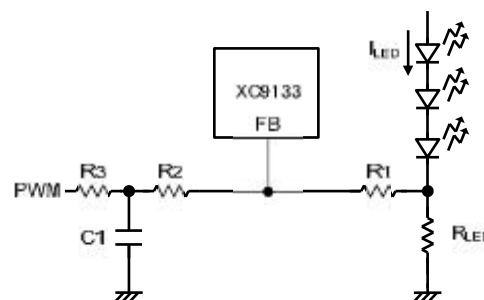


Figure Circuit inputting a PWM signal to the FB pin

### <Prevent Emission Caused by White LEDs Leakage>

When the input voltage ( $V_{IN}$ ) is high, minimum illumination may occur even if the CE pin is in the disable state. If this happens, please connect a transistor to between the LED and the FB pin. By driving the CE signal in-phase and cutting the pass to current, the minimum illumination can be prevented.

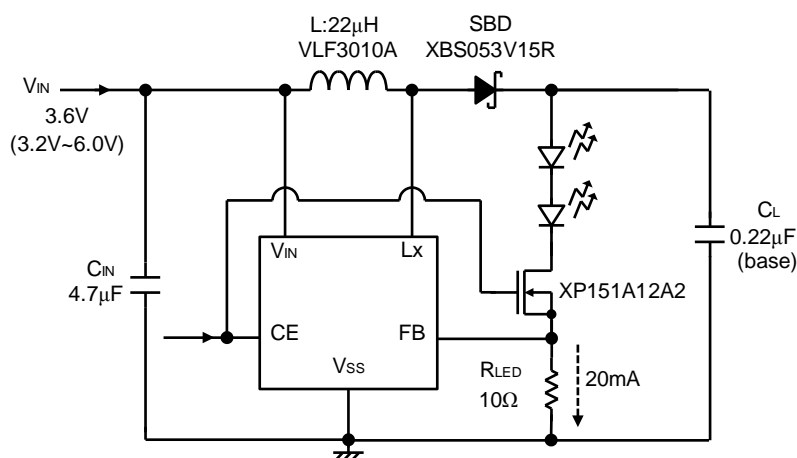


Figure Circuit Prevent Emission Caused by White LEDs Leakage

## ■ APPLICATION INFORMATION (Continued)

### <Illumination of Six in Total White LEDs>

It is possible to illuminate three-series two parallel white LEDs, six in total, using an input voltage  $V_{IN} \geq 3.2V$ .

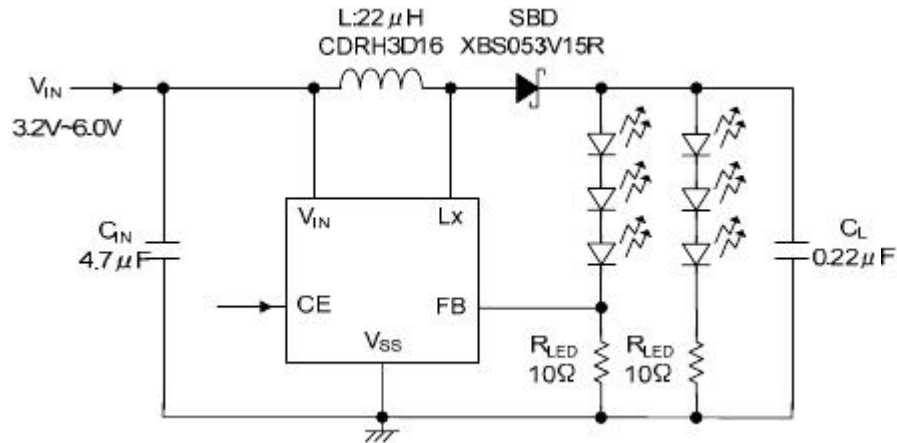


Figure Circuit Illumination of Six in Total White LEDs

### <Use as Flash>

An LED current 65mA (MAX.) can be supplied to two white LEDs.

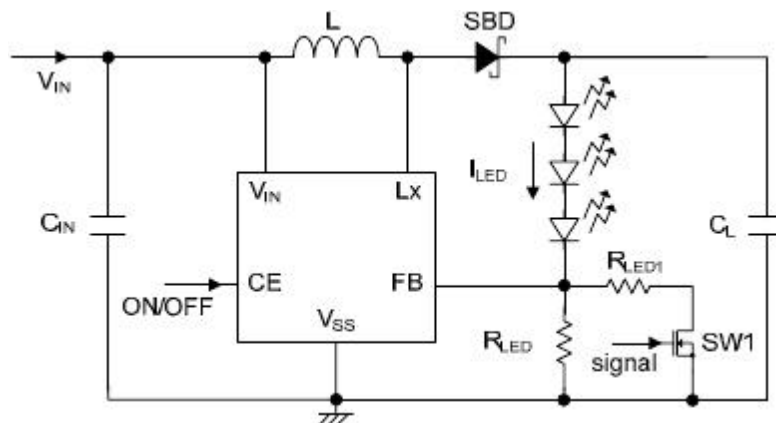
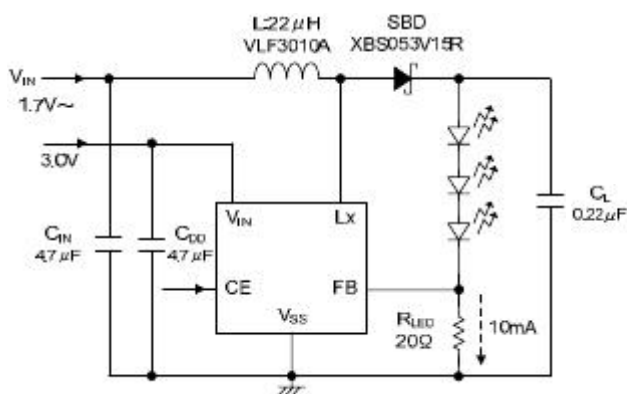


Figure Circuit using a Flash

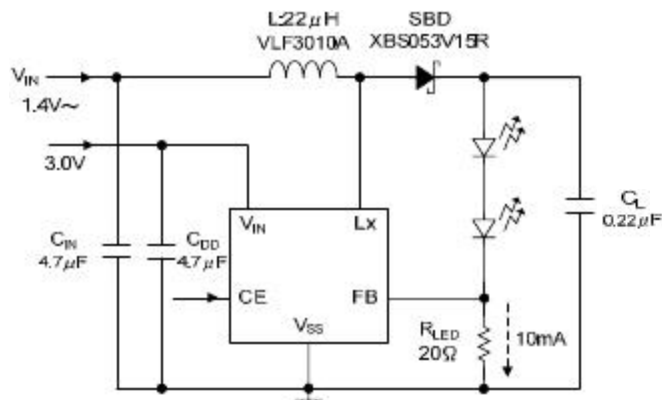
## ■APPLICATION INFORMATION (Continued)

<Separate Supply Source of the Step-up Circuit (VIN) from VIN Pin>

Supply source of the step-up circuit can be used separately from VIN pin.



Circuit example of separating supply source of the step-up circuit from VIN pin ( 3 LEDs)



Circuit example of separating supply source of the step-up circuit from VIN pin ( 2 LEDs)

Note: Please input 2.5V~6V to the VIN pin when you use.

<LED Open-circuit Protection>

If white LEDs are opened or damaged, the FB pin is pulled down, so that the operating duty ratio reaches the maximum.

Accordingly, the output voltage continues to increase, possibly causing the Lx pin voltage to eJTMTee the absolute maximum rating of 22V.

If white LEDs are opened or damaged, the detector built in the Lx pin causes the IC to stop oscillating, preventing eJTMTessive increase of the output voltage. However, the detector may detect an overvoltage if the Lx pin voltage eJTMTees 18V, which is the overvoltage limit, even when no LEDs are open. Therefore, care must be taken if four LEDs each having a forward voltage of 4.45V or more are connected in series.

<Startup Inrush Current>

The JTMT9133 series has no soft-start circuit built-in in order to minimize delay at startup. The inrush current can reach up to the current limit  $I_{LIM}$ .

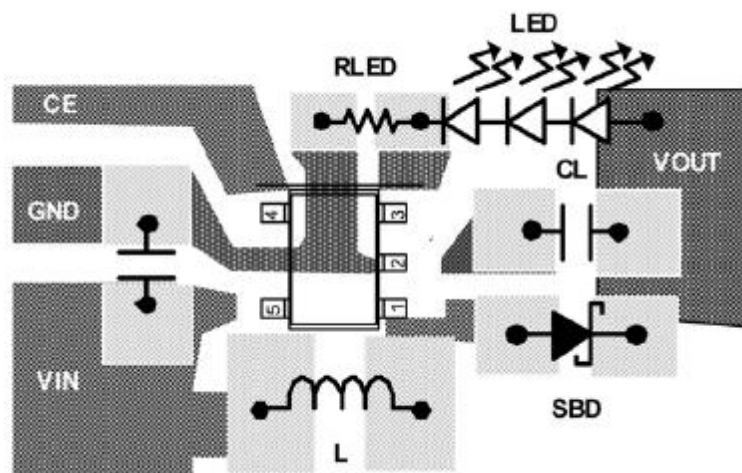
In some cases, overshoot can occur.

## ■APPLICATION INFORMATION (Continued)

### <Instruction on Pattern Layout>

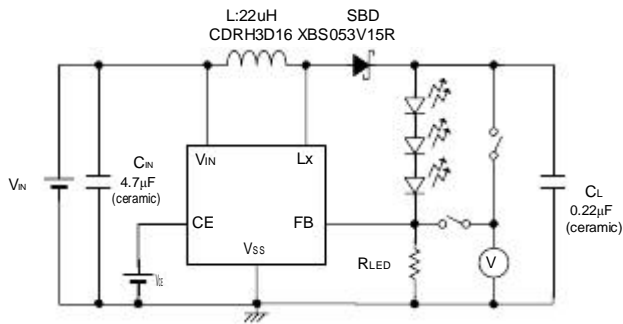
1. In order to stabilize  $V_{IN}$ 's voltage level, we recommend that an input by-pass capacitor ( $C_{IN}$ ) be connected as close as possible to the  $V_{IN}$  &  $V_{SS}$  pins.
2. Please mount each external component as close to the IC as possible.
3. Wire external components as close to the IC as possible and use thick, short connecting traces to reduce the circuit impedance.
4. Make sure that the PCB GND traces are as thick as possible, as variations in ground potential caused by high ground currents at the time of switching may result in instability of the IC.

### ●JTMT9133B Series Pattern Layout

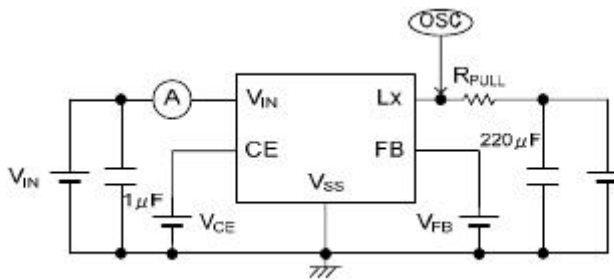


## ■TEST CIRCUITS

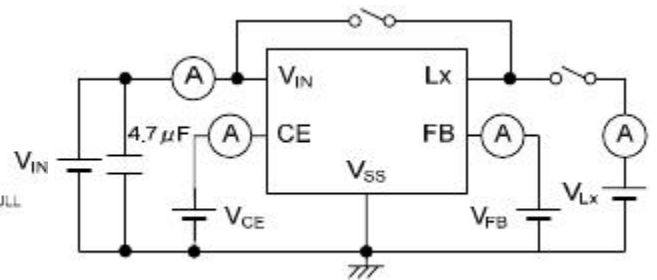
### ●Circuit ① (JTMT9133B02A Series)



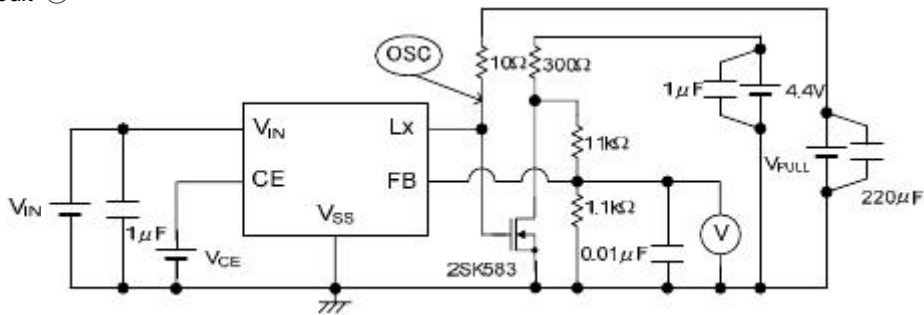
### ●Circuit ②



### ●Circuit ③



### ●Circuit ④



#### 1. The measurement method of Lx ON Resistance $R_{SWON}$

Using the circuit ④, Lx ON resistance can be measured by adjusting  $V_{PULL}$  voltage to set Lx voltage  $V_{Lx}$  0.4V when the driver transistor is ON.

The oscilloscope is used for measuring the Lx voltage when the driver transistor is ON.

$$R_{SWON} = 0.4 / ((V_{PULL} - 0.4) / 10)$$

#### 2. The measurement method of current limit $I_{LIM}$

Using the circuit ④, current limit  $I_{LIM}$  can be calculated by the equation including  $V_{PULL}$  voltage when FB voltage is decreased while  $V_{PULL}$  voltage is adjusted and Lx voltage  $V_{Lx}$  when the driver transistor is ON.

The oscilloscope is used for measuring the Lx voltage when the driver transistor is ON.

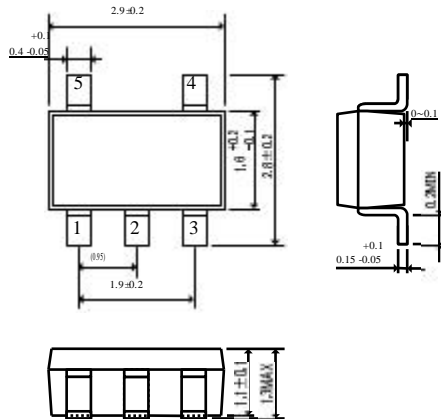
$$I_{LIM} = (V_{PULL} - V_{Lx}) / R_{PULL}$$

$$R_{PULL} = 10\Omega$$

## ■ PACKAGING INFORMATION

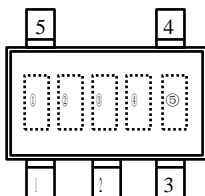
### ● SOT-25

(unit : mm)



## ■ MARKING RULE

SOT25<sub>5</sub>



① represents product series

MARK	PRODUCT SERIES
N	JTMT9133****M*

② represents Lx overvoltage limit

MARK	Lx OVERVOLTAGE LIMIT	PRODUCT SERIES
B	Available	JTMT9133****M*

③ represents oscillation frequency

MARK	OSCILLATION FREQUENCY	PRODUCT SERIES
A	1MHz	JTMT9133****M*

④⑤ represents production lot number

01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to Z9, ZA to ZZ repeated.

(G, I, J, O, Q, W eJTMTepted)

\* No character inversion used.

1. The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.
2. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.
3. Please ensure suitable shipping controls (including fail-safe designs and aging protection) are in force for equipment employing products listed in this datasheet.
4. The products in this datasheet are not developed, designed, or approved for use with such equipment whose failure of malfunction can be reasonably expected to directly endanger the life of, or cause significant injury to, the user.  
(e.g. Atomic energy; aerospace; transport; combustion and associated safety equipment thereof.)
5. Please use the products listed in this datasheet within the specified ranges.  
Should you wish to use the products under conditions exceeding the specifications, please consult us or our representatives.
6. We assume no responsibility for damage or loss due to abnormal use.
7. All rights reserved. No part of this datasheet may be copied or reproduced without the prior permission of JIATAIMU SEMICONDUCTOR LTD.

**TOREX SEMICONDUCTOR LTD.**