

3A ULTRA LOW DROPOUT LINEAR REGULATOR

FEATURES

- Ultra Low Dropout 0.21V(typical) @ 2A Output Current for 1.2V Output Voltage
- Low ESR Output Capacitor (Multi-layer Chip Capacitors (MLCC)) Applicable
- EN Pull-low for JTMA8803CMP-A
EN Pull-high for JTMA8803CMP-B
- Fast Transient Response
- Adjustable Output Voltage by External Resistors
- Power-On-Reset Monitoring on Both VCNTL and VIN Pins
- Internal Soft-Start
- Current-Limit and Thermal Shutdown Protection
- Power-OK Output with a Delay Time
- SO-8 with Exposed Pad Pb-Free & DFN 3x3-10L Package.
- Halogen Free Product

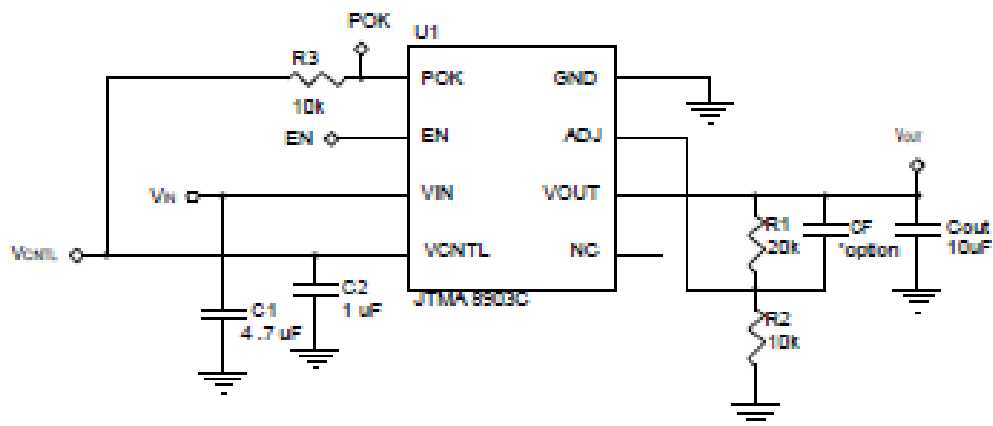
DESCRIPTION

The JTMA8903C series is a 3A ultra low dropout linear regulator. This product is specifically designed to provide well supply voltage for front-side-bus termination on motherboards and NB applications. The IC needs two supply voltages, a control voltage for the circuitry and a main supply voltage for power conversion, to reduce power dissipation and provide extremely low dropout. The JTMA8903C series integrates many functions. A Power-On-Reset (POR) circuit monitors both supply voltages to prevent wrong operations. A thermal shutdown and current limit functions protect the device against thermal and current over-loads. A POK indicates the output status with time delay which is set internally. It can control other converter for power sequence.

The JTMA8903C series can be enabled by other power system. Pulling and holding the EN pin below 0.4V shuts off the output.

The JTMA8903C series is available in DFN 3x3-10L & ESOP-8 package which features small size as SO-8 and an Exposed Pad to reduce the junction-to-case resistance.

TYPICAL APPLICATION



有需要详细的文件请发邮件: oumao18@jtm-ic.com 联系或电话: 0755-262580208